



Response Under 37 C.F.R. § 1.116  
Expedited Procedure  
Group Art Unit 2822

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION OF  
YAMADA et al.

Confirmation No.: 8085

Group Art Unit: 2822

Appln. No.: 09/993,967

Examiner: Lewis, Monica

Filed: November 27, 2001

Title: SEMICONDUCTOR DEVICE WITH VERTICAL TRANSISTOR FORMED IN A  
SILICON-ON-INSULATOR SUBSTRATE

November 10, 2003

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AMENDMENT UNDER 37 C.F.R. § 1.116

Mail Stop AF  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated July 10, 2003, please enter the following  
remarks:

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